



B1121MH MONITOR BURN-IN SYSTEM

B1121MH monitor burn-in system is a high capacity burn-in system for the memory and logic devices . There are 36 burn-in board slots in the system ,including 6 timing pattern generator . The System is controlled by pentium iii CPU , based on the OS/2 WARP operation system . The software affords a multi-work function through multiple-window and provides editing simultaneously programming function .

FEATURES

- One IBM pentium iii PC control the chamber equipped with independent power supply to each burn-in board for concurrent burn-in of burn-in device.
- Deep vector memory and wide bits holds patterns for ASIC and other devices.
- Waveform pattern editor provides a simple method to generate pattern or vectors.
- System controller continuously monitor all chamber positions and monitor all stress parameters including voltage, current, signal, and temperature.





Burn-in Chamber

- Temperature range : room temp to 150°C.
- Thermal gradient +3°C - 1°C.
- Temperature controller have two chamber, resolution 0.1°C..
- 36 burn-in slots , 6 independent zone.
- For logic, memory and embedded memory.

Driver Control Board

- Driver channel : 96CH (MAX).
- Driving capability : 100mA sink current.
: 100mA source current
- Tr and Tf : ≤50ns, @R=56 ohm, C=300PF, use diagnostic board
to measure. (Vih : 1.5V, 10% to 90%), Vil =0
- Comparator : 96CH (MAX).
- Tri-state control : per pin.
- Overshoot ≤ ±50mV ± (Vih * 10%) with fine tune C use by diagnostic board.
- Undershoot ≤ ±50mV ± (Vil * 10%) with fine tune C use by diagnostic board.
- Vih : 0.5V to 6V resolution 10mV
Accuracy : ≤ (Vout * 0.1%) ±30mV
- Vil : Fix 0V , less than 0.2V ; Rout <15 Ω
- Voh / Vol : 0V to 8V resolution 10mV
- Comparator : tpd < 25ns @ over driver voltage > 200mv
- Noise level : ≤ ±50mV ± (Vout * 10%) with fine tune R C use by
diagnostic board.
- Driver function : 96CH (MAX)
Address :32bit (min 16 bit)
Control clock :32bit (min 8 bit)
Data :32bit (max 64 bit)

POWER SUPPLY

- DPS1 : DC 0V TO 8V / 15A resolution 10mV, step 10mV
- DPS2 : DC 0V TO 8V /4.5A resolution 10mV, step 10mV
- DPS3 : DC -8V TO 8V /1A resolution 10mV, step 10mV
- ALL DPS With OVP 、 OCP 、 OTP ◦ (OVP 、 OCP Programmable , OTP fix)
Power noise : ≤ ±50mV ± (Vout * 10%)



ALPG (option)

Timing generator

On The Fly	: 16 timing sets
Clock phase	: per pin
Cycle time	: 200ns to 40us
Clock delay	: 0ns to 20.47us
Comparator strobe	: 0ns to 20.47us
Strobe method	: edge strobe
Resolution	: 50ns

Address generator

Address generator	: X=16 bit, Y=16bit
Pattern function	: +, -
Address mode	: normal, multiplex
Formatter	: RZ, NRZ, RTO, SBC

Data generator

Data bit	: 16 bit
Data pattern	: programmable
Data function	: AND, OR, XOR, $\times 2$, $\div 2$, +, -
Formatter	: RZ, NRZ, RTO, SBC

Vector generator

Vector width	: 48 bit, 96 bit (max)
Vector depth	: 128k
Repeat	: repeat the same vector 2 to 64K
Loop counter	: 4
Loop count	: 2 to 64K
Jump	: condition, unconditional
Formatter	: RZ, NRZ, RTO, SBC

CS generator

No. of signal	: 32
Formatter	: RZ, NRZ, RTO, SBC



Burn-in Controller

- Pentium iii 700 PC with 64M DRAM
- 20GB HD, 1.44MFD
- 15" VGA monitor
- Novellus application software

Software

- Auto zone monitor
- Burn-in pattern editor
- Burn-in plan editor
- System verify program
- Pattern converter
- Pattern file convert to hex code file
- Hex code convert to pattern
- Burn-in message view
- File purge
- Working directory define
- OS/2

Power Requirement

- 208V AC +-10% , 3 PHASE , 5 WIRE , 60HZ , 75A

System dimension

- 215cm x150cm x230cm (HxDxW)